



Bio Technology Algorithm Developer

Responsibilities

Research and development of proprietary sensor and fusion algorithms in bio-technology.

Requirements:

- Experience in development of algorithms for medical devices – MUST!
- Experience in designing sensor system – MUST!
- Dealt with complex projects from research to product.
- Strong signal processing skills, algorithms development & expert and **bio-medical** signals.
- Specialty in **medical** image processing algorithms.
- Familiar with many design environments, both software and hardware.
- Identify new directions for innovation.
- Defining the objective and goals of each milestone in order to achieve successful project compilation
- Auto Deduct, Strong analytical and problem solving skills, Innovation, Creativity & Entrepreneurial Spirit, Very well organized and comfortable working in a multi-tasking project-oriented environment.
- Matlab programming and implementing.

Education

MSc/PhD in physics, mathematics, bio-technology, Bio Medical Engineering, EE

Location: Ramat Gan

Please send your CV to: jobs.sirc@samsung.com



Image Processing, Machine Learning and Computer Vision Algorithms Engineer

Responsibilities

Develop Algorithms to extend and improve Samsung platforms and products.

Research and explore new directions and implement them.

Requirements:

- Advanced degree in CS / EE / Math / Physics
- Industry experience of at least 5 years
- Experience in Computer vision and Machine learning algorithms
- Experience and a good understanding of image processing algorithms, face detection, Pattern Recognition and motion detection
- Solid background in applied machine learning, data mining, statistics
- Developed systems based on convolutional neural networks
- Good coding skills
- Excellent communication and interpersonal skills

Location: Ramat Gan

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Digital Verification Engineer

Requirements:

- B.A. in Electronic Engineering, MSc. an advantage
- Experience of at least 5 years as verification engineer, completing at least 2 full chip development cycles
- Knowledge in verification methodologies, tools (simulators and relative APIs, coverage tools, accelerators, formal, etc.), and techniques
- Knowledge of Verilog or VHDL, System Verilog
- Experience in C and Perl programming
- Experience in building of verification environment from scratch, with the emphasis on verification methodology
- Experience and knowledge of System Verilog methodology is advantage
- Good knowledge of Unix environment and script languages
- Methodological approach to building of verification environment and test plan
- Methodological approach to the verification tasks planning and execution

Location: Ramat Gan

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VLSI Design Engineer

Requirements:

- BA in Electronic Engineering, MSc. an advantage
- Vast knowledge in design and verification tools and techniques
- Experience of at least 4 years
- Capable of taking responsibility for various VLSI tasks and bringing them to successful implementation.
- Understanding of the backend / circuit requirements is an advantage.
- Previous experience in the multimedia imaging is an advantage.
- Experience with Verilog coding – advantage

Location: Ramat Gan

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Student Position in Computer Vision Algorithm team

Requirements:

- Student of BSc/MSc/BA in computer science, electrical engineering or related fields.
- Good programming skills (mainly Matlab and C++)
- knowledge in image processing/computer vision/machine learning
- Good interpersonal skills.
- Great analytical skills

Location: Ramat Gan

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