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## Intel Haifa is looking for SOC Power management validation engineer

We have an opening for talented power management validation engineer, includes but not limited to:

- System level understanding of power management flows at the hardware, firmware, and driver levels
- In-depth level understanding of hardware power management such as P-states and C-states
- Analysis of silicon power consumption versus pre-silicon expectations and drive resolution of issues towards higher battery life
- Responsibility for SoC-level optimization under various usage scenarios
- Close interface with architecture, design, and platform engineers
- Contribute towards power management optimizations flows for future products.

### Job Requirements:

- BSC degree in Electrical Engineering or Computer Engineering.
- Track record of practical problem solving and documentation skills
- Excellent communication and/or interaction skills
- Details oriented
- Familiarity with CPU architecture
- Coding experience (Assembly, Python, C++, C#)

Apply at:  
<http://intel.ly/1DMO8CR>