

Digital Circuit Design Engineer-HDL

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Job ID	STG-0691476	Job type	Full-time Regular
Work country	Israel	Position type	Entry level
Work city	Haifa	Posted	11-Sep-2014
Travel	No travel	Job area	Engineering (hardware)
Business group	IBM Systems & Technology Group	Job category	Hardware Development & Support
Business unit	STG Other	Job role	Hardware Developer
Commissionable/ Sales-Incentive jobs only	No	Job role skillset	General

Job description

The designated candidate will participate in the physical implementation of the next IBM generation of CPU's, designed with IBM's latest and greatest Silicon process. The designated candidate will be using cutting edge tools and design methodology, like Large Block Synthesis. Yet, due to the design complexity and tight requirements, the person who will fill this position will require fundamental VLSI and digital circuit design understanding and knowhow. The candidate will be required to demonstrate excellent communication and interpersonal skills, to work effectively in a highly dynamic team environment, as a part of a larger global team.

Starting work date 1/1/2015.

Job Roles:

- Owning the physical design of a set of blocks from initial design phase to RIT
- Bring design blocks from RTL through automated synthesis process to create the Boolean equivalent physical implementation
- Working as a member of a design team
- Working closely with the Logic design team
- Working in the frame of a world global design team.

Requirements

- Thorough VLSI & digital circuit design knowledge
- Acquaintance with digital CAD tools and Linux like work environment
- Team player
- Motivated, self-directed, and able to work independently
- Up to 2 years from graduation of B.Sc or M.Sc

Advantages:

- Scripting (Shell/SKILL/PERL/TCL)
- RTL coding
- Processor's architecture
- Deep submicron technologies and basic device physics
- Custom design

Grades sheets must be added to the application.



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English

Required

- Bachelor's Degree
- Basic knowledge in VLSI & Digital Circuit Design Fundamentals
- English: Fluent
- Hebrew: Fluent

IBM is committed to creating a diverse environment and is proud to be an equal opportunity employer. All qualified applicants will receive consideration for employment without regard to race, color, religion, gender, gender identity or expression, sexual orientation, national origin, genetics, disability, age, or veteran status.

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Hardware Verification Engineer- HDL

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Job ID	STG-0691445	Job type	Full-time Regular
Work country	Israel	Position type	Entry level
Work city	Haifa	Posted	11-Sep-2014
Travel	No travel	Job area	Engineering (hardware)
Business group	IBM Systems & Technology Group	Job category	Hardware Development & Support
Business unit	STG Other	Job role	Hardware Developer
Commissionable/ Sales-Incentive jobs only	No	Job role skillset	General

Job description

IBM STG (System Technology Group) in Israel is looking for an outstanding EE or CS graduate

The position involves responsibility for planning and executing tests in a large scale and complex verification environment, in addition to customizing parts of the environment via coding in C++ and script language.

Development and support of a leading test generator tool used in the verification of processors during their design phase, via C++ coding.

The technology involved is at the backbone of IBM's processors development effort, including embedded processors, servers and super computers. As the complexity of processors grow, new technological developments and innovations are required. The work will be performed as part of a local and/or global verification team for an advanced, state of the art design.

Starting work date 1/1/2015.

Requirements:

- Up to 2 years from graduation of B.Sc or M.Sc.
- Excellent technical capabilities, understanding of complex technical challenges
- Excellent people and communication skills
- Ability to work as part of a team
- Self learning capabilities

Advantages:

- Understanding of computer architectures
- Knowledge and/or experience in object-oriented programming (C++)
- Knowledge in communication, IO, memory, and processors
- familiarity with verification methodology

Grades sheets must be added to the application.

Required

- Bachelor's Degree
- Basic knowledge in Verification Engineer
- English: Fluent
- Hebrew: Fluent

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