

# Curriculum Vitae

1 July, 2006

## Personal

Name: **Moiseev Konstantin** Address: Meyer bld. 771,  
Department of Electrical Engineering  
Technion, Haifa 32000 Israel  
Date of birth: October 23, 1978  
Country of birth: Ukraine E-mail address: [mkostya@tx.technion.ac.il](mailto:mkostya@tx.technion.ac.il)  
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## Education

- Ph. D. in Electrical Engineering, Haifa, Technion, ongoing  
Advisors: Dr. Avinoam Kolodny, Dr. Shmuel Wimer  
Research topic: "**Optimization of Migration Processes in Complex VLSI Design**"
- M. Sc. (cum laude) in Electrical Engineering, Haifa, Technion, 2005.  
Advisors: Dr. Shmuel Wimer, Dr. Avinoam Kolodny.  
Thesis title: "**Performance Optimization by Reordering of Interconnect Wires in VLSI**".
- B. Sc. (cum laude) in Computer Engineering, Haifa, Technion, 2000.

## Research Interests

- Interconnect in VLSI circuits
- Computer Aided Design of VLSI circuits

## Work Experience

### **Haifa, Technion, 2002-Current**

High Speed Digital Systems Laboratory, teaching assistant. Undergraduate projects supervising, giving lectures on VHDL, FPGA design tools and SoPC. Teaching Assistant in charge in "Integrated Circuits" and "VLSI CAD" courses. Designing interconnect timing optimization tool flow.  
Database programming in Magic environment. Software Engineer.  
Database programming in Magic environment.  
Teaching assistant in "Logic design" course.

### **Haifa, Intel Israel, 2004**

### **Petah-Tikva, MOST comp., 2003**

### **Min. of Defense, 2000-2003**

### **Haifa, Technion, 1998-2000**

## Military Service

### **2000-2003, Intelligence**

Database programming in Magic environment

## Skills / Abilities

- Worked with state-of-the-art design, simulation and analysis tools for ASICs
- Programmed in C, C++ programming languages, Windows and Unix operating systems, VxWorks RTOS
- Designed in VHDL
- Worked much with design, simulation, synthesis, implementation and debug tools for FPGA
- Designed with Xilinx SoPC tools
- Participated in different professional courses and workshops on programming, synthesis, FPGAs, RTOS.

## Languages

**Human** Russian (native), Hebrew (fluent), English (good)

**Computer** C/C++ (1996-2005), Java (2000), Magic (2000-2003), MATLAB (1996-2005), VHDL (2000, 2002-2005), Pascal (1993-1996, 1999-2000), Basic (1991-1996), Perl (2005), Tcl (2005), AWK (2004), some x86 assembly (1998), some LISP (1999, 2004).