List Decoding of Polar Codes

Ido Tal Alexander Vardy University of California San Diego, La Jolla, CA 92093, USA Email: idotal@ieee.org, avardy@ucsd.edu

Abstract—We describe a successive-cancellation list decoder for polar codes, which is a generalization of the classic successivecancellation decoder of Arıkan. In the proposed list decoder, up to L decoding paths are considered concurrently at each decoding stage. Simulation results show that the resulting performance is very close to that of a maximum-likelihood decoder, even for moderate values of L. Thus it appears that the proposed list decoder bridges the gap between successive-cancellation and maximum-likelihood decoding of polar codes.

The specific list-decoding algorithm that achieves this performance doubles the number of decoding paths at each decoding step, and then uses a pruning procedure to discard all but the L "best" paths. In order to implement this algorithm, we introduce a natural pruning criterion that can be easily evaluated. Nevertheless, straightforward implementation still requires $O(L \cdot n^2)$ time, which is in stark contrast with the $O(n \log n)$ complexity of the original successive-cancellation decoder. We utilize the structure of polar codes to overcome this problem. Specifically, we devise an efficient, numerically stable, implementation taking only $O(L \cdot n \log n)$ time and $O(L \cdot n)$ space.

I. INTRODUCTION

Polar codes, recently discovered by Arıkan [1], are a major breakthrough in coding theory. They are the first and currently only family of codes known to have an explicit construction and efficient encoding and decoding algorithms, while also being capacity achieving over binary input symmetric memoryless channels. Their probability of error is known to approach $O(2^{-\sqrt{n}})$ [2], with generalizations giving even better asymptotic results [3].

Of course, "capacity achieving" is an asymptotic property, and the main sticking point of polar codes to date is that their performance at short to moderate block lengths is disappointing. As we ponder why, we identify two possible culprits: either the codes themselves are inherently weak at these lengths, or the successive cancellation (SC) decoder employed to decode them is significantly degraded with respect to Maximum Likelihood (ML) decoding performance. More so, the two possible culprits are complementary, and so both may occur.

In this paper we show an improvement to the SC decoder, namely, a successive cancellation list (SCL) decoder. Our list decoder has a corresponding *list size* L, and setting L = 1 results in the classic SC decoder. As can be seen in Figure 1, our algorithm improves upon the classic SC decoder. Indeed, Figure 1 shows a wide range in which our algorithm has performance very close to that of the ML decoder¹, even for



Fig. 1. Word error rate of a length n = 2048 (top) and n = 8192 (bottom) rate 1/2 polar code optimized for SNR=2 dB under various list sizes. Code construction was carried out via the method proposed in [4].

moderate values of L. Thus as we have suspected, the suboptimality of the SC decoder plays a role in the disappointing performance of polar codes.

The structure of this paper is as follows. In Section II, we present Arıkan's SC decoder in a notation that will be useful to us later on. In Section III, we show how the space complexity of the SC decoder can be brought down from $O(n \log n)$ to O(n). This observation will later help us in Section IV, where we presents our successive cancellation list decoder with time complexity $O(L \cdot n \log n)$.

II. FORMALIZATION OF THE SUCCESSIVE CANCELLATION DECODER

The Successive Cancellation decoder is due to Arıkan [1]. In this section, we recast it using our notation, for future reference. However, let us start by stating some conventions and defining the underlying bit channels [1].

Let the polar code under consideration have length $n = 2^m$ and dimension k. Thus, the number of frozen bits is n-k. We denote by $\mathbf{u} = (u_i)_{i=0}^{n-1}$ the information bits vector (including the frozen bits), and by $\mathbf{c} = (c_i)_{i=0}^{n-1}$ the corresponding codeword, which is sent over a binary-input channel $W : \mathcal{X} \to \mathcal{Y}$, where $\mathcal{X} = \{0, 1\}$. At the other end of the channel, we get the received word $\mathbf{y} = (y_i)_{i=0}^{n-1}$. A decoding algorithm is

¹The lower bound on ML was got by performing decoding with L = 32, and counting the number of times the decoded codeword was more likely than the transmitted one.

then applied to y, resulting in a decoded codeword \hat{c} having corresponding information bits \hat{u} .

For layer $0 \leq \lambda \leq m$, denote hereafter $\Lambda = 2^{\lambda}$. Recall that for $0 \leq \varphi < 2^{\lambda}$, bit channel $W_{\lambda}^{(\varphi)}$ is a binary input channel with output alphabet $\mathcal{Y}^{\Lambda} \times \mathcal{X}^{\varphi}$, the conditional probability of which we generically denote as $W_{\lambda}^{(\varphi)}(\mathbf{z}_{0}^{\Lambda-1}, \mathbf{u}_{0}^{\varphi-1} | u_{\varphi})$. In our context, $\mathbf{z}_{0}^{\Lambda-1}$ is always a contiguous subvector of \mathbf{y} .

For $1 \leq \lambda \leq m$, recall the recursive definition of a bit channel [1, Equations (22) and (23)] : let $0 \leq 2\psi < \Lambda$, then

$$W_{\lambda}^{(2\psi)}(\mathbf{z}_{0}^{\Lambda-1}, \mathbf{u}_{0}^{2\psi-1} | u_{2\psi}) = \sum_{u_{2\psi+1}} \frac{1}{2} \underbrace{W_{\lambda-1}^{(\psi)}(\mathbf{z}_{0}^{\Lambda/2-1}, \mathbf{u}_{0,\text{even}}^{2\psi-1} \oplus \mathbf{u}_{0,\text{odd}}^{2\psi-1} | u_{2\psi} \oplus u_{2\psi+1})}_{\text{even branch}} \underbrace{W_{\lambda-1}^{(\psi)}(\mathbf{z}_{\Lambda/2}^{\Lambda-1}, \mathbf{u}_{0,\text{odd}}^{2\psi-1} | u_{2\psi+1})}_{(1)}$$

odd branch

and

$$W_{\lambda}^{(2\psi+1)}(\mathbf{z}_{0}^{\Lambda-1}, \mathbf{u}_{0}^{2\psi} | u_{2\psi+1}) = \frac{1}{2} \underbrace{W_{\lambda-1}^{(\psi)}(\mathbf{z}_{0}^{\Lambda/2-1}, \mathbf{u}_{0,\text{even}}^{2\psi-1} \oplus \mathbf{u}_{0,\text{odd}}^{2\psi-1} | u_{2\psi} \oplus u_{2\psi+1})}_{\text{even branch}}_{\cdot \underbrace{W_{\lambda-1}^{(\psi)}(\mathbf{z}_{\Lambda/2}^{\Lambda-1}, \mathbf{u}_{0,\text{odd}}^{2\psi-1} | u_{2\psi+1})}_{\text{odd branch}}$$
(2)

with "stopping condition" $W_0^{(0)}(z|u) = W(z|u)$.

For $0 \leq \lambda \leq m$ define the following quotient/remainder shorthand. Let $0 \leq \varphi < 2^{\lambda}$ and $0 \leq \beta < 2^{m-\lambda}$, then

 $\langle \varphi, \beta \rangle_{\lambda} = \varphi + 2^{\lambda} \cdot \beta$.

Note that each integer $0 \le i < 2^m$ has a unique representation as $i = \langle \varphi, \beta \rangle_{\lambda}$. For reasons which will become clear, we call φ and β the *phase* and *branch* parts of *i*, respectively. Thus, we will say that layer λ has 2^{λ} phases, each phase consisting of $2^{m-\lambda}$ branches.

Our first implementation of the SC decoder (Algorithms 1-3) will be straightforward, but somewhat wasteful in terms of space. It will make use of two sets of arrays. The first set of arrays is defined as follows. For each $0 \le \lambda \le m$, we will have a *probabilities array*, denoted by P_{λ} , of length 2^m , indexed by an integer $0 \leq i < 2^m$. Each element $P_{\lambda}[i]$ will contain a probability pair, indexed as $P_{\lambda}[i][0]$ and $P_{\lambda}[i][1]$. The second set consists of *bit arrays*, denoted by B_{λ} . They have the same length and indexing of probability arrays. However, each element $B_{\lambda}[i]$ of a bit array consists of a single bit. Initially, most array elements will be uninitialized, and will become initialized as the algorithm runs its course. Note that the total space needed for these arrays is $O(n \log n)$. In the interest of brevity, for a generic array A we abbreviate $A_{\lambda}[\langle \varphi, \beta \rangle_{\lambda}]$ as $A_{\lambda}[\langle \varphi, \beta \rangle]$. Also, we use the shorthand $A_{\lambda}[\langle \varphi, * \rangle]$ to symbolize all the elements $A_{\lambda}[\langle \varphi, \beta \rangle]$ for $0 \leq \beta < 2^{m-\lambda}$.

Due to space limitations, we will not give a full proof of the correctness of our implementation, but rather a short explanation. For $\lambda > 0$ and $0 \le \varphi < 2^{\lambda}$, recall the recursive definition of $W_{\lambda}^{(\varphi)}(\mathbf{z}_{0}^{\Lambda-1}, \mathbf{u}_{0}^{\varphi-1}|u_{\varphi})$ given in either (1) or (2),

Algorithm 1: First implementation of SC decoder

$$\begin{array}{l} \mathbf{1} \ \mathbf{for} \ \beta = 0, 1, \dots, n-1 \ \mathbf{do} \ // \ \text{Initialization} \\ \mathbf{2} \ \left[\begin{array}{c} P_0[\langle 0, \beta \rangle][0] \leftarrow W(y_\beta|0), \ P_0[\langle 0, \beta \rangle][1] \leftarrow W(y_\beta|1) \\ \mathbf{3} \ \mathbf{for} \ \varphi = 0, 1, \dots, n-1 \ \mathbf{do} \ // \ \text{Main loop} \\ \mathbf{4} \ \left[\begin{array}{c} \text{recursivelyCalcP}(m, \varphi) \\ \mathbf{5} \ \mathbf{if} \ \hat{u}_{\varphi} \ is \ \textit{frozen then} \\ \left[\begin{array}{c} \text{set} \ B_m[\langle \varphi, 0 \rangle] \ \mathbf{b} \ \mathbf{forzen value} \\ \mathbf{7} \ \mathbf{else} \\ \mathbf{8} \\ \mathbf{10} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11} \\ \mathbf{12} \\ \mathbf{11} \\ \mathbf{11}$$

13 return the decoded codeword:
$$\hat{\mathbf{c}} = (B_0[\langle 0, \beta \rangle])_{\beta=0}^{n-1}$$

Algorithm 2: recursivelyUpdateB(
$$\lambda, \varphi$$
)

 Require: φ is odd

 1 set $\psi \leftarrow \lfloor \varphi/2 \rfloor$

 2 for $\beta = 0, 1, \dots, 2^{m-\lambda} - 1$ do

 3 $\begin{vmatrix} B_{\lambda-1}[\langle \psi, 2\beta \rangle] \leftarrow B_{\lambda}[\langle \varphi - 1, \beta \rangle] \oplus B_{\lambda}[\langle \varphi, \beta \rangle] \end{vmatrix}$

 4 $\begin{vmatrix} B_{\lambda-1}[\langle \psi, 2\beta + 1 \rangle] \leftarrow B_{\lambda}[\langle \varphi, \beta \rangle] \end{vmatrix}$

 5 if $\psi \mod 2 = 1$ then

 6 \lfloor recursivelyUpdateB($\lambda - 1, \psi$)

depending on the parity of φ . In both cases, the channel $W_{\lambda-1}^{(\psi)}, \psi = \lfloor \varphi/2 \rfloor$ is used with two different outputs. Thus, we need a simple way of defining which set of outputs we are referring to. We do this by specifying, apart from the layer λ and the phase φ which define the channel, the branch number $0 \leq \beta < 2^{m-\lambda}$. Since the channel $W_m^{(\varphi)}$ has only one vector pair of outputs associated with it, $(\mathbf{y}_0^n, \hat{\mathbf{u}}_0^{\varphi-1})$, we give a branch number of $\beta = 0$ to each such pair. Next, we proceed recursively as follows. Consider a channel $W_{\lambda}^{(\varphi)}$ with outputs $(\mathbf{z}_0^{\Lambda-1}, \mathbf{u}_0^{\varphi-1})$ and corresponding branch number β . The output $(\mathbf{z}_0^{\Lambda/2-1}, \mathbf{u}_{0,\text{even}}^{2\psi-1} \oplus \mathbf{u}_{0,\text{odd}}^{2\psi-1})$ associated with $W_{\lambda-1}^{(\psi)}$ will have a branch number of 2β , while the output $(\mathbf{z}_{\Lambda/2}^{\Lambda-1}, \mathbf{u}_{0,\text{odd}}^{2\psi-1})$ will

Algorithm 3: recursivelyCalcP (λ, φ)

have a branch number of $2\beta + 1$ (recall the even branch/odd branch naming). Similarly to tagging the output of a channel by the branch number β , we do the same for the input to it.

III. SPACE-EFFICIENT SUCCESSIVE CANCELLATION DECODING

The running time of the SC decoder is $O(n \log n)$, and our implementation is no exception. As we have previously noted, the space complexity of our algorithm is $O(n \log n)$ as well. However, we will now show how to bring the space complexity down to O(n). The observation that one can reduce the space complexity to O(n) was noted, in the context of VLSI design, in [5].

As a first step towards this end, consider the probability pair array P_m . By examining the main loop in Algorithm 1, we quickly see that if we are currently at phase φ , then we will never again make use of $P_m[\langle \varphi', 0 \rangle]$ for all $\varphi' < \varphi$. On the other hand, we see that $P_m[\langle \varphi'', 0 \rangle]$ are uninitialized for all $\varphi'' > \varphi$. Thus, instead of reading and writing to $P_m[\langle \varphi, 0 \rangle]$, we can essentially disregard the phase information, and use only the first element $P_m[0]$ of the array, discarding all the rest. By the recursive nature of polar codes, this observation — disregarding the phase information — can be exploited for a general layer λ as well. Specifically, for all $0 \le \lambda \le m$, let us now define the number of elements in P_{λ} to be $2^{m-\lambda}$. Accordingly, we must also replace all references of the generic form $P_{\lambda}[\langle \varphi, \beta \rangle]$ by $P_{\lambda}[\beta]$.

Note that the total space needed to hold the P arrays has gone down from $O(n \log n)$ to O(n). We would now like to do the same for the B arrays. However, as things are currently stated, we can not disregard the phase, as can be seen for example in line 3 of Algorithm 2. The solution is a simple renaming. As a first step, let us define for each $0 \le \lambda \le m$ an array C_{λ} consisting of bit pairs and having length n/2. Next, let a generic reference of the form $B_{\lambda}[\langle \varphi, \beta \rangle]$ be replaced by $C_{\lambda}[\psi + \beta \cdot 2^{\lambda-1}][\varphi \mod 2]$, where $\psi = |\varphi/2|$. Note that we have done nothing more than rename the elements of B_{λ} as elements of C_{λ} . However, we now see that as before we can disregard the value of ψ and take note only of the parity of φ . So, let us make one more substitution: replace $C_{\lambda}[\psi + \beta \cdot$ $2^{\lambda-1}[\varphi \mod 2]$ by $C_{\lambda}[\beta][\varphi \mod 2]$, and resize each array C_{λ} to have $2^{m-\lambda}$ bit pairs. The alert reader will notice that a further reduction in space is possible, since for $\lambda = 0$ we will always have that $\varphi = 0$ and thus its parity is always even. However, this reduction does not affect the asymptotic space complexity which is now indeed down to O(n).

The main loop of the new algorithm is given as Algorithm 4. The helper functions are given as Algorithms 5 and 6, with the added condition of ignoring the ℓ parameter (defined and used in the next section) and any lines making reference to it.

We end this subsection by mentioning that although we were concerned here with reducing the *space* complexity of our SC decoder, the observations made with this goal in mind will be of great use in analyzing the *time* complexity of our list decoder.

Algorithm 4: Space efficient SC decoder, main loop Input: the received vector y

Output: a decoded codeword \hat{c}

// Initialization 1 for $\beta = 0, 1, \dots, n-1$ do 2 set $P_0[\beta][0] \leftarrow W(y_\beta|0), P_0[\beta][1] \leftarrow W(y_\beta|1)$ // Main loop **3** for $\varphi = 0, 1, ..., n - 1$ do recursivelyCalcP (m, φ) 4 if \hat{u}_{φ} is frozen then 5 set $C_m[0][\varphi \mod 2]$ to the frozen value 6 7 else if $P_m[0][0] > P_m[0][1]$ then 8 set $C_m[0][\varphi \mod 2] \leftarrow 0$ 9 10 else set $C_m[0][\varphi \mod 2] \leftarrow 1$ 11 if $\varphi \mod 2 = 1$ then 12 recursivelyUpdateC (m, φ) 13

14 return the decoded codeword: $\hat{\mathbf{c}} = (C_0[\beta][0])_{\beta=0}^{n-1}$

Algorithm 5: recursivelyCalcP(λ, φ, ℓ)

	// stopping condition
1	if $\lambda = 0$ then return
2	set $\psi \leftarrow \lfloor \varphi/2 \rfloor$
	// Recurse first, if needed
3	if $\varphi \mod 2 = 0$ then recursively $CalcP(\lambda - 1, \psi)$
	// Perform the calculation
4	$P_{\lambda} \leftarrow \texttt{getArrayPointer}_P(\lambda, \ell)$
5	$P_{\lambda-1} \leftarrow \text{getArrayPointer}_P(\lambda-1,\ell)$
6	for $\beta = 0, 1,, 2^{m-\lambda} - 1$ do
7	if $\varphi \mod 2 = 0$ then // apply Equation (1)
8	for $u' \in \{0, 1\}$ do
9	$ P_{\lambda}[\beta][u'] \leftarrow$
	$\sum_{u''} \frac{1}{2} P_{\lambda-1}[2\beta][u' \oplus u''] \cdot P_{\lambda-1}[2\beta+1][u'']$
10	else // apply Equation (2)
11	$C_{\lambda} \leftarrow \texttt{getArrayPointer}(\lambda, \ell)$
12	set $u' \leftarrow C_{\lambda}[\beta][0]$
13	for $u'' \in \{0,1\}$ do
14	$P_{\lambda}[\beta][u''] \leftarrow$
	$\left \begin{array}{c} \frac{1}{2}P_{\lambda-1}[2\beta][u'\oplus u'']\cdot P_{\lambda-1}[2\beta+1][u''] \right.$

Algorithm 6	recursivelyUpda	te $\mathrm{C}(\lambda,arphi,\ell)$
-------------	-----------------	-------------------------------------

Require: φ is odd

1 set $C_{\lambda} \leftarrow \text{getArrayPointer}_{C}(\lambda, \ell)$ 2 set $C_{\lambda-1} \leftarrow \text{getArrayPointer}_{C}(\lambda-1, \ell)$ 3 set $\psi \leftarrow \lfloor \varphi/2 \rfloor$ 4 for $\beta = 0, 1, \dots, 2^{m-\lambda} - 1$ do 5 $\begin{bmatrix} C_{\lambda-1}[2\beta][\psi \mod 2] \leftarrow C_{\lambda}[\beta][0] \oplus C_{\lambda}[\beta][1] \\ C_{\lambda-1}[2\beta+1][\psi \mod 2] \leftarrow C_{\lambda}[\beta][1] \end{bmatrix}$ 7 if $\psi \mod 2 = 1$ then 8 $\parallel \text{ recursivelyUpdateC}(\lambda - 1, \psi)$



Fig. 2. Decoding paths of unfrozen bits for L = 4.

IV. SUCCESSIVE CANCELLATION LIST DECODER

In this section we introduce and define our algorithm, the successive cancellation list (SCL) decoder. Our list decoder has a parameter L, called the *list size*. Generally speaking, larger values of L mean lower error rates but longer running times. We note at this point that successive cancellation list decoding is not a new idea: it was applied in [6] to Reed-Muller codes².

Recall the main loop of an SC decoder, in which at each phase we must decide on the value of \hat{u}_{φ} . In an SCL decoder, instead of deciding to set the value of an unfrozen \hat{u}_{φ} to either a 0 or a 1, we inspect both options. Namely, at each phase, when decoding a non-frozen bit, we split each decoding path into two paths (see Figure 2). Of course, since the number of paths grows exponentially, we must prune them, and the maximum number of paths allowed is the specified list size, *L*. Naturally, we would like to keep the best paths at each stage, and thus require a pruning criterion.

Consider the following outline for a naive implementation of an SCL decoder. Each time a decoding path is split in two, the data structures used by the "parent" path are duplicated, with one copy given to the first split and the other to the second. Since the number of splits occurring is $O(L \cdot n)$, and since the size of the data structures used by each path is at least O(n), the copying operation alone would take time at least $O(L \cdot n^2)$. This running time is clearly impractical for all but the shortest of codes. However, all known (to us) implementations of successive cancellation list decoding have complexity at least $O(L \cdot n^2)$. Our main contribution in this section is the following: we show how to implement SCL decoding with time complexity $O(L \cdot n \log n)$ instead of $O(L \cdot n^2)$.

The key observation is as follows. Consider the P arrays of the last section, and recall that the size of P_{λ} is proportional to $2^{m-\lambda}$. Thus, the cost of copying P_{λ} grows exponentially small with λ . On the other hand, when looking at the main loop of Algorithm 4 and unwinding the recursion, we see that P_{λ} is accessed only every $2^{m-\lambda}$ incrementations of φ in Algorithm 4. Put another way, the bigger P_{λ} is, the less frequently it is accessed. The same observation applies to the C arrays. This observation suggest the use of a "lazy copy". Namely, At each given stage, the same array may be *flagged* as belonging to more than one decoding path. However, when a given decoding path needs access to an array it is sharing with another path, a copy is made. The previous high level description translates into quite a bit of book-keeping, through the use of auxiliary arrays and queues. Unfortunately, space limitations prevent us from furnishing all of the relevant pseudo code in this paper. Algorithms 5–10 are all that we have managed to fit in, and we hope the names of the missing functions give a good hint as to what they do. Likewise, we do not elaborate on how we have chosen the pruning function. Lastly, if one were to try to implement our pseudo code, they would quickly discover numerical problems; specifically underflow. This can be overcome through careful normalization of the probabilities. We close by promising that a full version of this paper, with all the missing details and explanations, will be posted on arXiv.

Algorithm 7: assignInitialPath

- inactivePathIndices ← new queue
 inactiveArrayIndices ← new array of size m, the elements of which are queues
 activePath ← new boolean array of size L
- 4 arrayReferenceCount \leftarrow new 2-D array of size $m \times L$ 5 pathIndexToArrayIndex \leftarrow new 2-D array of size $m \times L$
- 6 arrayPointer $P \leftarrow new 2$ -D array of size $m \times L$, the
- elements of which are array pointers $m \times L$, the
- 7 arrayPointer_C \leftarrow new 2-D array of size $m \times L$, the elements of which are array pointers

```
// Initialization of data structures
```

- 8 for $\lambda = 0, 1, \ldots, m$ do
- 9 for $s = 0, 1, \dots, L 1$ do
- 10 arrayPointer_P[λ][s] \leftarrow new array of float pairs of size $2^{m-\lambda}$ 11 arrayPointer_C[λ][s] \leftarrow new array of bit pairs
 - of size $2^{m-\lambda}$
- 12 arrayReferenceCount $[\lambda][s] \leftarrow 0$

```
13 push(inactiveArrayIndices[\lambda], s)
```

```
14 for \ell = 0, 1, \dots, L - 1 do
```

```
15 | activePath[\ell] \leftarrow false
```

- 16 **push**(inactivePathIndices, ℓ)
 - // Get the new path index, and mark its
 arrays
- 17 $\ell \leftarrow \mathbf{pop}(\mathsf{inactivePathIndices})$

```
18 activePath[\ell] \leftarrow true
```

```
19 for \lambda = 0, 1, ..., m do
```

```
20 | s \leftarrow \mathbf{pop}(\mathsf{inactiveArrayIndices}[\lambda])
```

```
21 pathIndexToArrayIndex[\lambda][\ell] \leftarrow s
```

```
22 arrayReferenceCount[\lambda][s] \leftarrow 1
```

```
23 return \ell
```

Algorithm 8: continuePaths_FrozenBit(φ)

1 for $\ell = 0, 1, \dots, L - 1$ do

- 2 | if pathIndexInactive(ℓ) then continue
- 3 | $C_m \leftarrow \text{getArrayPointer}_C(m, \ell)$
- 4 set $C_m[0][\varphi \mod 2]$ to the frozen value of index φ

 $^{^{2}}$ In a somewhat different version of successive cancellation than that of Arıkan's, at least in exposition.

Algorithm 9: continuePaths_UnfrozenBit(φ)			
1	forksArray \leftarrow new (float, bit, index)-triplets array of size $2L$		
2	$i \leftarrow 0$		
	// populate forksArray		
3	for $\ell = 0, 1,, L - 1$ do		
4	if pathIndexInactive(ℓ) then continue		
5	$P_m \leftarrow \text{getArrayPointer}(m, \ell)$		
6	forksArray $[2i] \leftarrow (P_m[0][0], 0, \ell)$		
7	forksArray $[2i+1] \leftarrow (P_m[0][1], 1, \ell)$		
8	$i \leftarrow i + 1$		
	// pivot forksArray, possible in $O(L)$ time		
9	$\rho \leftarrow \min(2i, L)$		
10	rearrange the entries of forksArray so that for all $\alpha < \rho$		
	and $\beta \ge \rho$ we have that for $k \ge \Lambda$ area $[-1][0] \ge for k \ge \Lambda$ area $[-2][0]$		
	$[0]$ $[A]$ $[A]$ $[0] \ge [0]$ $[A]$		
11	ρ rick the Dest ρ IOLKS contForks \leftarrow new (boolean boolean) pairs array of size		
11	L		
12	initialize all elements of contForks to (false false)		
13	for $r = 0, 1, \dots, \rho - 1$ do		
14	$\ell \leftarrow \text{forksArrav}[r][2]$		
15	$b \leftarrow \text{forksArray}[r][1]$		
16	contForks $[\ell][b] \leftarrow true$		
	/ Kill-off non-continuing naths		
17	for $\ell = 0.1$ $L = 1$ do		
18	$i\mathbf{f}$ pathIndexInactive(ℓ) then		
19	continue		
20	└ if		
20	contForks[ℓ][0] = false and contForks[ℓ][1] = false		
	then		
21	$ $ killPath(ℓ)		
	// Continue relevant paths, and		
	duplicate if necessary		
22	for $\ell = 0, 1, \dots, L-1$ do		
23	II contEarly $[\ell][0]$ follow and contEarly $[\ell][1]$ follow		
	then $(/$ both forks are had on invalid		
24	continue		
24			
25	$C_m \leftarrow \text{getArrayPointer}_C(m, \ell)$		
26	II CONTFORKS $[\ell][0]$ = true and CONTFORKS $[\ell][1]$ = true		
	unen // both forks are good		
27	$ \qquad set \ C_m[0][\varphi \mod 2] \leftarrow 0$		
28	$\ell \leftarrow \text{clonePatn}(\ell)$		
29	$\bigcup_{m \leftarrow \text{getArrayPointer}_C(m, \ell)} \bigcup_{m \leftarrow getAr$		
30	$ set \cup_m[0][\varphi \mod 2] \leftarrow 1$		
31	if contEorks[l][0] - true then		
32	$ \qquad \qquad \text{set } C_m[0][\omega \mod 2] \leftarrow 0$		

34

35

else

set $C_m[0][\varphi \mod 2] \leftarrow 1$

Algorithm 10: SCL decoder, main loop

Input: the received vector **y** and a list size L as a global **Output:** a decoded codeword $\hat{\mathbf{c}}$ // Initialization 1 $\ell \leftarrow assignInitialPath()$ 2 P_0 ← getArrayPointer_P(0, ℓ) 3 for $\beta = 0, 1, ..., n - 1$ do 4 | set $P_0[\beta][0] \leftarrow W(y_\beta|0), P_0[\beta][1] \leftarrow W(y_\beta|1)$ // Main loop **5** for $\varphi = 0, 1, ..., n - 1$ do for $\ell = 0, 1, \dots, L - 1$ do 6 7 if pathIndexInactive(ℓ) then continue 8 recursivelyCalcP (m, φ, ℓ) 9 10 if \hat{u}_{φ} is frozen then continuePaths_FrozenBit(φ) 11 12 else continuePaths_UnfrozenBit(φ) 13 if $\varphi \mod 2 = 1$ then 14 for $\ell = 0, 1, ..., L - 1$ do 15 if pathIndexInactive(ℓ) then 16 continue 17 recursivelyUpdateC (m, φ, ℓ) 18 // Find the best codeword in the list 19 $\ell' \leftarrow 0, p' \leftarrow 0$ 20 for $\ell = 0, 1, \dots, L - 1$ do if pathIndexInactive(ℓ) then 21 continue 22 $C_m \leftarrow \text{getArrayPointer}(m, \ell)$ 23 $P_m \leftarrow \text{getArrayPointer}_P(m, \ell)$ 24 25 if $p' < P_m[0][C_m[0][1]]$ then 26 27 set $C_0 \leftarrow \text{getArrayPointer}(0, \ell')$ 28 return $\hat{\mathbf{c}} = (C_0[\beta][0])_{\beta=0}^{n-1}$

REFERENCES

- [1] E. Arıkan, "Channel polarization: A method for constructing capacityachieving codes for symmetric binary-input memoryless channels," IEEE Trans. Inform. Theory, vol. 55, pp. 3051-3073, 2009.
- [2] E. Arıkan and E. Telatar, "On the rate of channel polarization," in Proc. IEEE Int'l Symp. Inform. Theory (ISIT'2009), Seoul, South Korea, 2009, pp. 1493-1495.
- [3] S. B. Korada, E. Şaşoğlu, and R. Urbanke, "Polar codes: Characterization of exponent, bounds, and constructions," IEEE Trans. Inform. Theory, vol. 56, pp. 6253-6264, 2010.
- [4] I. Tal and A. Vardy, "How to construct polar codes," arXiv:1105.6164v1.
- [5] C. Leroux, I. Tal, A. Vardy, and W. J. Gross, "Hardware architectures for successive cancellation decoding of polar codes," arXiv:1011.2919v1.
- [6] I. Dumer and K. Shabunov, "Soft-decision decoding of Reed-Muller codes: recursive lists," IEEE Trans. Inform. Theory, vol. 52, pp. 1260-1266, 2006.